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3	INZZ	1 AND cost	unrestricted	4	show titles
4	INZZ	1 AND Internet	unrestricted	0	-
5	INZZ	1 AND Intranet	unrestricted	0	-
6	INZZ	semicondutor ADJ design	unrestricted	0	-

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1	BRS	L89	184	character adj projection	USPAT; IBM_TDB	2005/06/07 13:33
2	BRS	L90	32	(character adj projection) same aperture	USPAT; IBM_TDB	2005/06/07 13:33
3	BRS	L91	1	((character adj projection) same aperture) and (circuit adj designs)	USPAT; IBM_TDB	2005/06/07 13:34


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[PDF] [Microsoft PowerPoint - R_TOSHIBA_A.ppt](#)

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Character projection writing system. for Mask making. Shaping deflection ...

 Writing **circuit**. CP **aperture**. Writing control. software. (Job d e c k) ...

www.sematech.org/resources/litho/meetings/mask/20010711/R_TOSHIBA_A.pdf - [Similar pages](#)

[SPIE Handbook, Volume 1: Microlithography, Section 2.10](#)

 ... An extension of PYRAMID for **circuit patterns** of arbitrary size," J. Vac. Sci.

 ... system EX-8D employing **character projection** exposure method," J. Vac. ...

www.cnf.cornell.edu/cnf_spie10.html - 78k - [Cached](#) - [Similar pages](#)

[United States Patent Application: 0020162088](#)

 ... by only the pattern of the character **aperture** mask as ... [0100] Thus, the combination

patterns of adjacent ... For example, a flip-flop **circuit** requires two shots in ...

appft1.uspto.gov/.../10/28/2002-%3E11/04/2002&RS=PD/20021028-%3E20021104 - 74k - Supplemental Result

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 ... writing (LEEDW) system with a common **character projection** (CP) **aperture**. ...

 The experimental results for our POC system indicated that the **patterns** ...

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 The system consists of an illumination optics, **character projection** apertures,

 ... To avoid the influence of the CP **aperture** fabrication accuracy on the ...

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 in ultrafine processing technology, which forms the critical **circuit patterns**.

 ... いるが、当社はCP (**Character Projection**) 方式の低加速電 ...

www.toshiba.co.jp/tech/review/2004/08/59_08pdf/a03.pdf - [Similar pages](#)

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 Balasubramanian NS **Circuit** ideas: separating p-waves in ... Fabrication process

 of **character projection**. mask for EB lithography. H01L 021/30 ...

www.wipo.org/scit/en/jopal/1998/jun.pdf - [Similar pages](#)





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 ... An extension of PYRAMID for **circuit patterns** of arbitrary ... JC Nabity, MN Wybourne,

 "A versatile pattern generator for ... a specially tailored Si **aperture**," J. Vac. ...

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aperture circuit patterns "character p"

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2	INZZ	'2' AND semiconductor	unrestricted	162673	show titles
3	INZZ	2 AND aperture	unrestricted	561	show titles
4	INZZ	1 AND 3	unrestricted	0	-
5	INZZ	2 AND 3	unrestricted	561	show titles
6	INZZ	1 AND aperature	unrestricted	0	-
7	INZZ	1 AND semicoductor	unrestricted	0	-

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... Ochiai, Norikazu; to **Kabushiki Kaisha Toshiba** Image recording medium ...

www.uspto.gov/web/patents/patog/week50/OG/patentee/alphaO.htm - 62k - [Cached](#) - [Similar pages](#)

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Patents & Designs Journal 12 May 2004 No. 5999 Application ...

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... toy vehicles, collector's cases, action playsets, paint by number sets, puzzles,

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... 1997/02/03. **KABUSHIKI KAISHA TOSHIBA**, ALSO TRADING AS TOSHIBA CORPORATION, 1-1, Shibaura, 1- chome, Minato-Ku, Tokyo 105-8001, JAPAN Representative for Service ...

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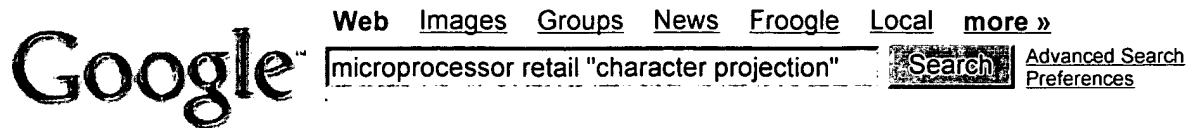
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1	BRS	67815	(Antrim Designs Systems).as.
2	BRS	3	("Antrim Designs Systems").as.
3	BRS	2	(multiple adj test adj bench)
4	BRS	0	(multiple adj test adj bench)
5	BRS	106	(parallel adj simulation)
6	BRS	13	(parallel adj simulation) same circuit
7	BRS	0	((parallel adj simulation) same circuit) and optimize\$
8	BRS	0	((parallel adj simulation) same circuit) and optimize
9	BRS	13	((parallel adj simulation) same circuit) and time
10	BRS	13	((parallel adj simulation) same circuit)
11	BRS	6	((parallel adj simulation) same circuit) and performance
12	BRS	0	((parallel adj simulation) same circuit) and (performance near measurement)
13	BRS	0	(parallel adj simulation) same (performance near measurement)
14	BRS	0	(parallel adj simulation) same (performance and measurement)
15	BRS	267403	(parallel adj simulation) sand (performance and measurement)
16	BRS	21	(parallel adj simulation) and (performance and measurement)
17	BRS	5	(parallel adj simulation) and (performance near measurement)
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19	BRS	7	(SmartSignal).as.
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21	BRS	0	(metamodeling same validate)
22	BRS	0	metamodeling same design
23	BRS	0	metamodeling and validate
24	BRS	5	metamodeling
25	BRS	2	703/2.ccls. and metamodel
26	BRS	1	703/2.ccls. and metamodel and validation and compat\$
27	BRS	184	character adj projection
28	BRS	32	(character adj projection) same aperture
29	BRS	1	((character adj projection) same aperture) and (circuit adj designs)
30	BRS	160	(semiconductor same design) and (semiconductor same (Internet or Intranet))
31	BRS	1	(semiconductor same design) and (semiconductor same (Internet or Intranet)) and (character adj projection)
32	BRS	0	(semiconductor near2 design) and (semiconductor same (Internet or Intranet)) and (character adj projection)
33	BRS	5406	(semiconductor near2 design)
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35	BRS	0	(semiconductor near2 design) and (design same Internet) and (character adj projection)
36	BRS	86	(semiconductor near2 design) and (design same Internet)

37	BRS	55	(semiconductor near2 design) and (design same Internet) and (character)
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38	BRS	0	(semiconductor near2 design) and (design same Internet) and ((character) same projection)
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40	BRS	86	(semiconductor near2 design) and (design same Internet)
41	BRS	11	(semiconductor near2 design) and (design same Internet) and (cost same delivery)
42	BRS	11	(semiconductor near design) and (design same Internet) and (cost same delivery)
43	BRS	18	(semiconductor near2 design) and (design same Internet) and cost and delivery
44	BRS	1	"6560768".pn. and Internet
45	BRS	1	"6560768".pn. and design
46	BRS	1	"6560768".pn. and design and cost
47	BRS	0	"6560768".pn. and design and cost and deliveryh
48	BRS	0	"6560768".pn. and design and cost and delivery
49	BRS	1	"6560768".pn. and design and cost and time
50	BRS	106	(delivery adj time) same Internet
51	BRS	3	((delivery adj time) same Internet) and (semiconductor same design)
52	BRS	0	((delivery adj time) same Internet) and (semiconductor same designers)
53	BRS	0	((delivery adj time) same Internet) and (semiconductor near designers)
54	BRS	0	(delivery same Internet) and (semiconductor near designers)
55	BRS	0	(delivery near Internet) and (semiconductor near designers)
56	BRS	2518	(semiconductor near cost)
57	BRS	6	(semiconductor near cost) and (semiconductor near delivery)
58	BRS	6	(semiconductor near cost) and (semiconductor near delivery)
59	BRS	0	(semiconductor near cost) and (semiconductor near delivery) and Internet
60	BRS	1353	(semiconductor and price and projection)
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76	BRS	3	(character adj projection) and microprocessor and Internet
77	BRS	5	(character adj projection) and microprocessor and (sale or retail or transactions)
78	BRS	0	(character adj projection) and microprocessor same (sale or retail or transactions)
79	BRS	5	(character adj projection) and microprocessor and (sale or retail or transactions)
80	BRS	567	"703"/\$.ccls. and Internet
81	BRS	163	"703"/\$.ccls. and Internet and (microprocessors)
82	BRS	0	"703"/\$.ccls. and Internet and (microprocessors) and (character adj projection)
83	BRS	3454	"703"/\$.ccls. and I(character adj projection)
84	BRS	0	"703"/\$.ccls. and (character adj projection)



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